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a latch responsive to said logic circuit for producing a first enable signal for controlling the powering up of a first voltage supply.

470. The device of claim 469 wherein said reset circuit comprises:
a plurality of series-connected buffers with a first one of said buffers responsive to said first output signal; and
a logic circuit responsive to said first output signal and a last one of said series-connected buffers.

471. The device of claim 470 wherein said reset circuit is constructed such that the first external signal must remain within a predetermined range for approximately one hundred nanoseconds for said logic circuit to output said first output signal.

REMARKS

This amendment presents new claim 466 – 471 for examination. No new matter has been added.

A clean copy of the claims currently pending is enclosed.

STATEMENT REQUESTING DELETION OF INVENTOR

At the time this application was filed, a request was made to delete the following inventors:

Raymond J. Beffa
Frank K. Ross
Larry D. Kinsman
Ronald L. Taylor
John S. Mullin

Upon further review, it has been determined that Scott J. Derner should also be deleted as he is not an inventor of the subject matter of this divisional application. Please file the instant application in the names of the remaining inventors (Keeth and Bunker) in accordance with 37 CFR 1.63 (d).

It is respectfully requested that the instant application, presenting claims 443 – 447, 449-451, and 453-471 receive an early office action on the merits.

Respectfully submitted

A handwritten signature in black ink, appearing to read 'E. L. Pencoske', with a stylized flourish at the end.

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